

Amendments to the Claims:

1. (original) A storage virtualization computer system comprising:

a host entity for issuing IO requests;

5 an external storage virtualization controller coupled to said host entity for executing
IO operations in response to said IO requests; and
at least one physical storage device(PSD), each coupled to the storage virtualization
controller through a point-to-point serial-signal interconnect, for providing
storage to the storage virtualization computer system through the storage
virtualization controller.

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2. (original) The storage virtualization computer system of claim 1 wherein said
point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

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3. (original) The computer system of one of claims 1 and 2, wherein a said at least
one PSD comprises a SATA PSD.

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4. (original) The computer system of one of claims 1 and 2, wherein a said at least
one PSD comprises a PATA PSD and a serial-to-parallel converter is provided between
said device-side IO device interconnect controller and said PATA PSD.

5. (original) The computer system of one of claims 1 and 2 further comprising a
detachable canister attached to said storage virtualization controller for containing a said
at least one PSD therein.

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6. (original) The computer system of one of claims 1 and 2, wherein a said at least
one PSD can be detached from said storage virtualization controller when said storage
virtualization controller is on-line.

7. (original) The computer system of one of claims 1 and 2, wherein a said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

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8. (original) The computer system of one of claims 1 and 2, wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of at least one said PSD.

10 9. (original) The storage virtualization computer system of one of claims 1 and 2 wherein said storage virtualization controller comprises:

a central processing circuitry for performing IO operations in response to said IO requests of said host entity;

15 at least one IO device interconnect controller coupled to said central processing circuitry;

at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and

20 at least one device-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to a said at least one physical storage device.

25 10. (original) The storage virtualization computer system of claim 9 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in the same IO device interconnect controller.

11. (original) The storage virtualization computer system of claim 9 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port

are provided in different IO device interconnect controllers.

12. (original) The computer system of one of claims 1 and 2, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.
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13. (original) The computer system of claim 12, wherein said storage virtualization controller is configured to present redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

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14. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

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15. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

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16. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

17. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

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18. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

19. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

5 20. (original) The computer system of claim 9, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

21. (original) A storage virtualization subsystem for providing storage to a host entity, comprising:

10 an external storage virtualization controller for connecting to the host entity and executing IO operations in response to IO requests issued from said host entity; and
at least one physical storage device, each coupled to the storage virtualization controller through a point-to-point serial-signal interconnect, for providing storage to the host entity through the storage virtualization controller.
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22. (original) The storage virtualization subsystem of claim 21 wherein said point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

20 23. (original) The storage virtualization subsystem of one of claims 21 and 22 wherein said storage virtualization controller comprises:

a central processing circuitry for performing IO operations in response to said IO requests of said host entity;

at least one IO device interconnect controller coupled to said central processing circuitry;

at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and

at least one device-side IO device interconnect port provided in a said at least one

IO device interconnect controller for coupling to a said at least one physical storage device.

24. (original) The storage virtualization subsystem of claim 23 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in the same IO device interconnect controller.
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25. (original) The storage virtualization subsystem of claim 23 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in different IO device interconnect controllers.
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26. (original) The storage virtualization subsystem of claim 23, wherein a said at least one PSD comprises a SATA PSD.

15 27. (original) The storage virtualization subsystem of claim 23, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.

20 28. (original) The storage virtualization subsystem of claim 23, wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of said at least one PSD.

25 29. (original) The storage virtualization subsystem of claim 27, wherein said storage virtualization controller is configured to present redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

30. (original) The storage virtualization subsystem of claim 23, wherein said at least one PSD comprises a PATA PSD and a serial-to-parallel converter is provided

between said device-side IO device interconnect controller and said PATA PSD.

31. (original) The storage virtualization subsystem of claim 23 further comprising a detachable canister attached to said storage virtualization controller for containing a said PSD therein.
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32. (original) The storage virtualization subsystem of claim 23, wherein at least one PSD can be detached from said storage virtualization controller when said storage virtualization controller is on-line.

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33. (original) The storage virtualization subsystem of claim 23, wherein a said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

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34. (original) The storage virtualization subsystem of claim 23, wherein said storage virtualization controller further comprises at least one multiple-device device-side expansion port for accommodating an additional set of at least one PSD.

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35. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

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36. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

37. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop

connectivity in target mode.

38. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

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39. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

10 40. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

15 41. (original) The storage virtualization subsystem of claim 23, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

42. (original) The storage virtualization subsystem of claim 23 further comprising an enclosure management services mechanism.

20 43. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism manages and monitors at least one of the following devices belonging to the storage virtualization subsystem: power supplies, fans, temperature sensors, voltages, uninterruptible power supplies, batteries, LEDs, audible alarms, PSD canister locks, door locks.

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44. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration.

45. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support device-forwarded EMS configuration.

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46. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

10 47. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support SES enclosure management services protocol.

15 48. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support SAF-TE enclosure management services protocol.

20 49. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises I2C latches to communicate with said storage virtualization controller.

50. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises status-monitoring circuitry to communicate with said storage virtualization controller.

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51. (original) ...The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises the following mechanism to communicate with said storage virtualization controller: I2C latches and status-monitoring circuitry.

52. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises a CPU for running a program.

5 53. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises at least one I2C interconnect as a primary communication media to said storage virtualization controller.

10 54. (withdrawn) An external storage virtualization controller for executing IO operations in response to IO requests from a host entity, comprising:

a central processing circuitry for performing IO operations in response to said IO requests of said host entity;

at least one IO device interconnect controller coupled to said central processing circuitry;

15 at least one host-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to said host entity; and

at least one device-side IO device interconnect port provided in a said at least one IO device interconnect controller for coupling to and performing point-to-point serial-signal transmission with at least one physical storage device.

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25 55. (withdrawn) The external storage virtualization controller of claim 54 wherein said device-side IO device interconnect controller comprises at least one Serial ATA port, each for connecting to a said at least one physical storage device through a Serial ATA IO device interconnect.

56. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein a said host-side IO device interconnect port and a said device-side IO

device interconnect port are provided in the same IO device interconnect controller.

57. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein a said host-side IO device interconnect port and a said device-side IO device interconnect port are provided in different IO device interconnect controllers.

10 58. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein said device-side IO device interconnect controller further comprises a PCI/PCI-X interface for connecting to the central processing circuit.

15 59. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 wherein said device-side IO device interconnect controller further comprises a PCI Express interface for connecting to the central processing circuit.

60. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.

20 61. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller is configured for defining at least one logical media unit consisting of sections of said at least one PSD.

25 62. (withdrawn) The external storage virtualization controller of claim 60, wherein said storage virtualization controller is configured for presenting redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

63. (withdrawn) The external storage virtualization controller of one of claims 54

and 55, wherein at least one said PSD is a direct-access storage device (DASD) and said storage virtualization controller is configured for defining a logical media unit consisting of sections of at least one said direct access storage device and said logical media unit is of RAID level or a combination of RAID levels, whereby said logical media unit is
5 contiguously addressable by said host entity.

64. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising at least one multiple-device device-side expansion port for accommodating a second set of at least one PSD.

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65. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

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66. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

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67. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

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68. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

69. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is ethernet

supporting the iSCSI protocol operating in target mode.

70. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial-Attached
5 SCSI (SAS) operating in target mode.

71. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

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72. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising an enclosure management services mechanism.

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73. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration.

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74. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support device-forwarded EMS configuration.

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75. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

76. (withdrawn) The storage virtualization controller of claim 72, wherein said storage virtualization controller is configured to support SES enclosure management services protocol.

77. (withdrawn) The storage virtualization controller of claim 72, wherein said storage virtualization controller is configured to support SAF-TE enclosure management services protocol.

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78. (original) A method for performing storage virtualization in a computer system with an external storage virtualization controller of the computer system, comprising:

receiving an IO request from a host entity of the computer system with the storage virtualization controller;

10 parsing the IO request with the storage virtualization controller to decide at least one IO operation to perform in response to said IO request; and
 performing at least one IO operation with the storage virtualization controller to access at least one physical storage device of the computer system in point-to-point serial-signal transmission.

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79. (original) The method of claim 78 wherein the point-to-point serial-signal transmission is performed in a format complying with a Serial ATA protocol.

80. (original) The method of one of claims 78 and 79 further comprising the step of
20 providing an enclosure management services mechanism.

81. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support direct-connect EMS configuration.

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82. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support device-forwarded EMS configuration.

83. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

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84. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SES enclosure management services protocol.

10 85. (original) The method of claim 80, further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SAF-TE enclosure management services protocol.

15 86. (original) The method of one of claims 78 and 79, wherein at least one said PSD comprises a SATA PSD.

87. (original) The method of one of claims 78 and 79, wherein at least one said PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

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88. (original) The method of one of claims 78 and 79, wherein the step of performing said at least one IO operation comprises issuing at least one device-side IO request to said device-side IO device interconnect controller and re-formatting said device-side IO request and accompanying IO data into at least one data packet for 25 transmission.

89. (original) The method of claim 88, wherein said data packet comprises a start segment at the beginning indicating the start of said data packet, an end segment at the

end indicating the end of the data packet, a payload data segment containing actual IO information to transmit through the device-side IO device interconnect, and a check data segment containing check codes derived from said payload data for checking the correctness of said payload data after transmission.

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90. (original) A computer-readable storage medium having a computer program code stored therein that is capable of causing a computer system having an external storage virtualization controller and at least one physical storage device connected to the storage virtualization controller to perform the steps of:

10 receiving an IO request from a host entity of the computer system with the storage virtualization controller;
parsing the IO request with the storage virtualization controller to decide at least one IO operation to perform in response to said IO request; and
performing said at least one IO operation with the storage virtualization controller to
15 access said at least one physical storage device in point-to-point serial-signal transmission.

20 91. (original) The computer-readable storage medium of claim 90 wherein the point-to-point serial-signal transmission is performed in a format complying with a Serial ATA protocol.

92. (original) The computer-readable storage medium of one of claims 90 and 91, wherein a said PSD comprises a SATA PSD.

25 93. (original) The computer-readable storage medium of one of claims 90 and 91, wherein a said PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

94. (original) The computer-readable storage medium of one of claims 90 and 91, wherein the step of performing said at least one IO operation comprises issuing a device-side IO request to said device-side IO device interconnect controller and 5 re-formatting said device-side IO request and accompanying IO data into at least one data packet for transmission.

95. (original) The computer-readable storage medium of claim 94, wherein said data packet comprises a start segment at the beginning indicating the start of said data packet, 10 an end segment at the end indicating the end of the data packet, a payload data segment containing actual IO information to transmit through the device-side IO device interconnect, and a check data segment containing check codes derived from said payload data for checking the correctness of said payload data after transmission.